

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Ernest Chen al.

Serial No.: 10

10/532,632

Filing Date:

April 25, 2005

For: LOWER COMPLEXITY LAYERED MODULATION SIGNAL PROCESSOR

Group Art Unit: 2611

Examiner:

BAYARD, Emmanuel

Attorney Docket No. PD-202130

Customer No. 020991

Certificate of Mailing under 37 CFR 1.8

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Janet Shibata

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## UNDER 37 C.F.R. § 1.97 (b)(4)

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Dear Sir:

The references cited on the enclosed form PTO/SB/08A may be relevant or material to the examination of the above-identified patent application and are, therefore, submitted in compliance with the duty of disclosure defined in 37 C.F.R. §1.56.

This statement should be considered because it is submitted before the mailing of a first Office action after the filing of a request for continued examination under §1.114 and therefore it is believed that no fee is due with this submission under 37 C.F.R. §1.97(b)(4).

If a fee is due, the Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment, to Deposit Account No. 50-0383 of Hughes Electronics Corporation, El Segundo, California.

This Information Disclosure Statement is not to be construed as a representation that a search has recently been made, that additional information material to the examination of this application does not exist, or that this citation constitutes prior art.

Respectfully submitted,

By:

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April 7, 2008